

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

April 15, 2005

Applicant : Ioan Dancea
Application/Control No. : 09/874,027
Title : METHOD AND VLSI CIRCUITS ALLOWING TO CHANGE
DYNAMICALLY THE LOGICAL BEHAVIOUR
Art Unit : Simulation and Emulation, Art Unit 2128
Examiner : FRED FERRIS, Patent Examiner
U. S. Patent and Trademark Office
Randolph Building, Room 5D19
401 Dulany Street
ALEXANDRIA, VA 22313
U.S.A.

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Reply in 3 month from the mailing date of the Examiner

Sir:

Response to the Examiner's action dated February 03, 2005

The Applicant would like to thank the Examiner for his attention to this application and for the comprehensive Report dated February 03, 2005.

Please enter the following amendments in the above mentioned application for purposes of immediate allowance.

IN THE DRAWINGS and ITS SPECIFICATIONS:

Please delete Figure 10 concerning the expert system.

Please delete the specification that describes the expert system.

Please replace Figure 11 with new Figures 10, 11 and 12.